

FIG.1

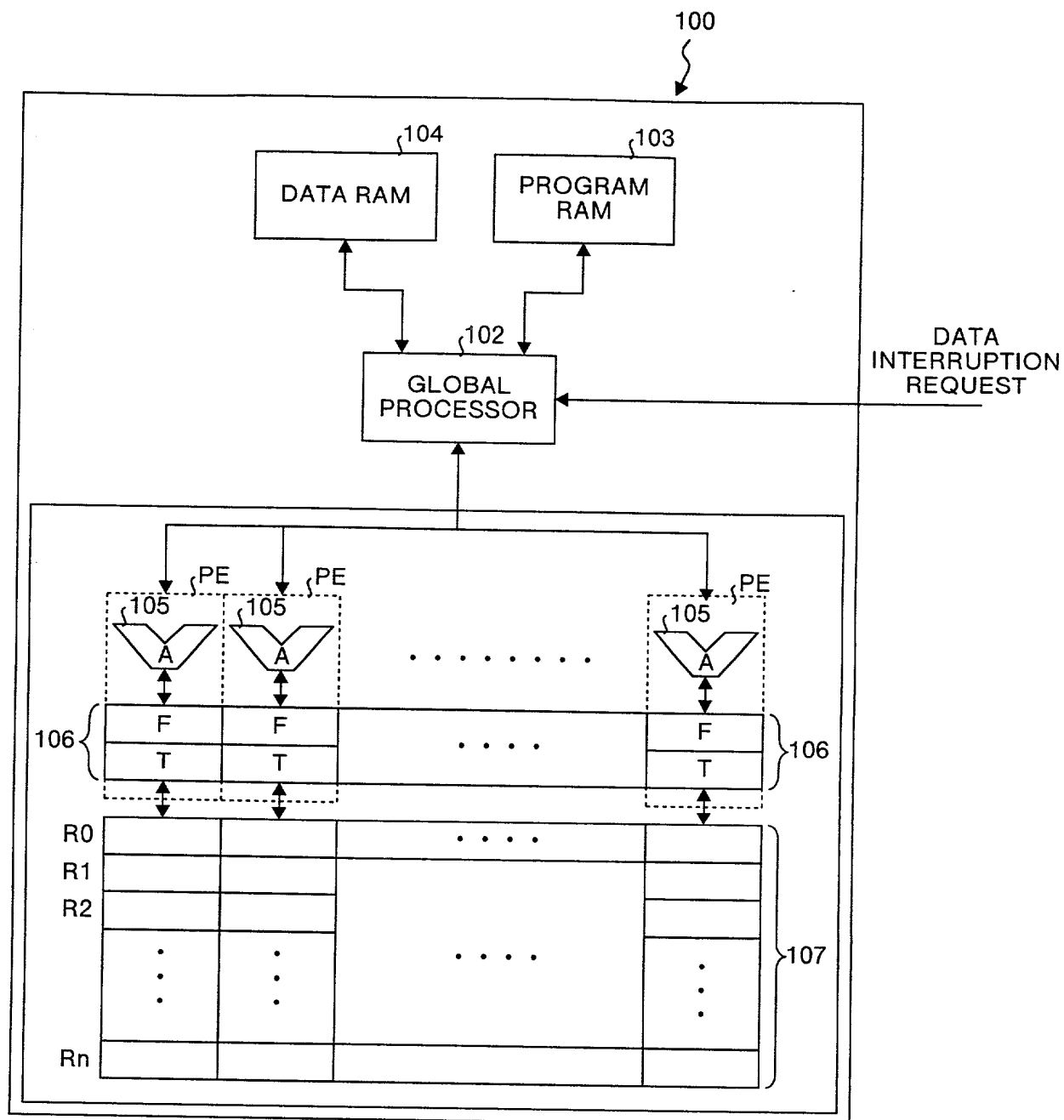
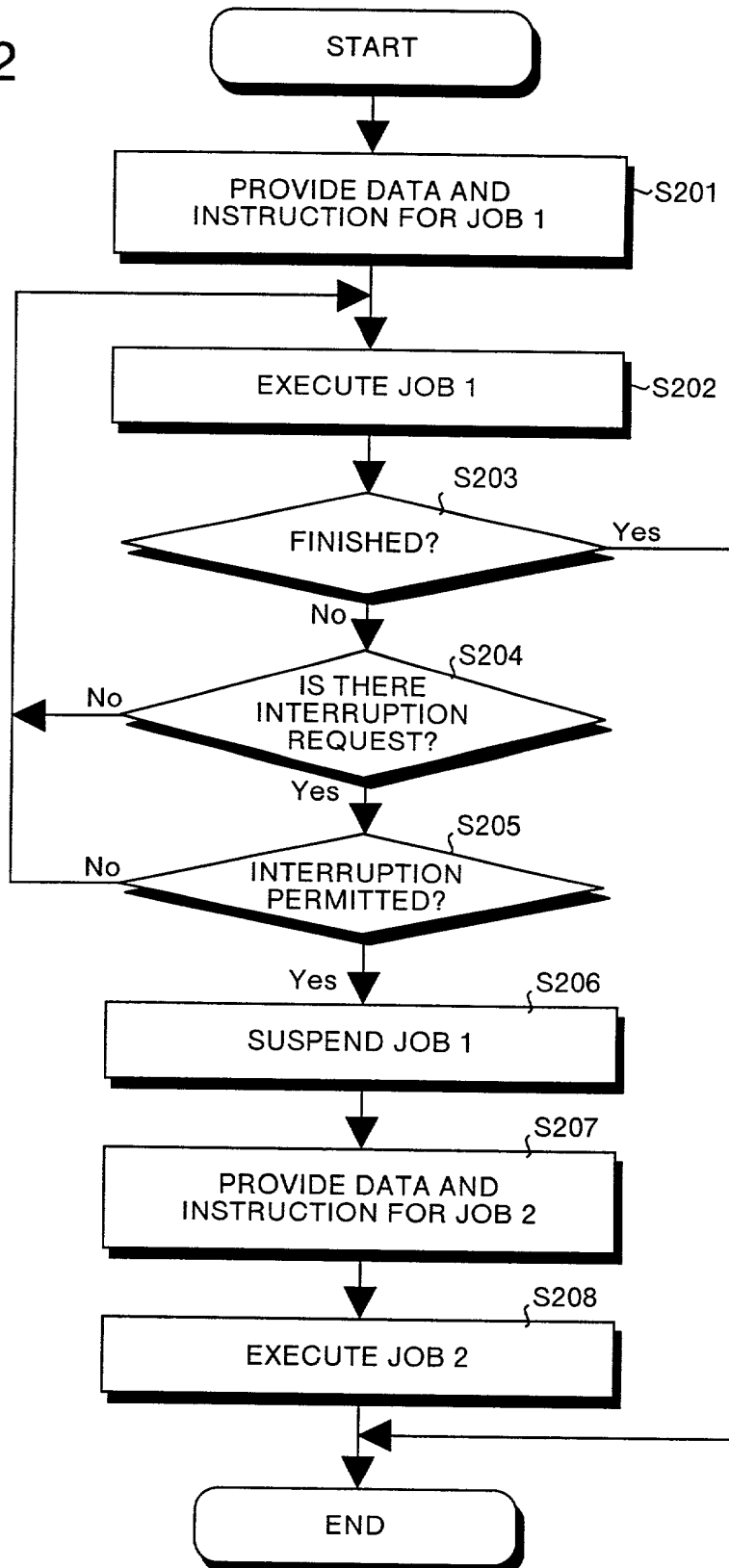


FIG.2



3/34

FIG.3

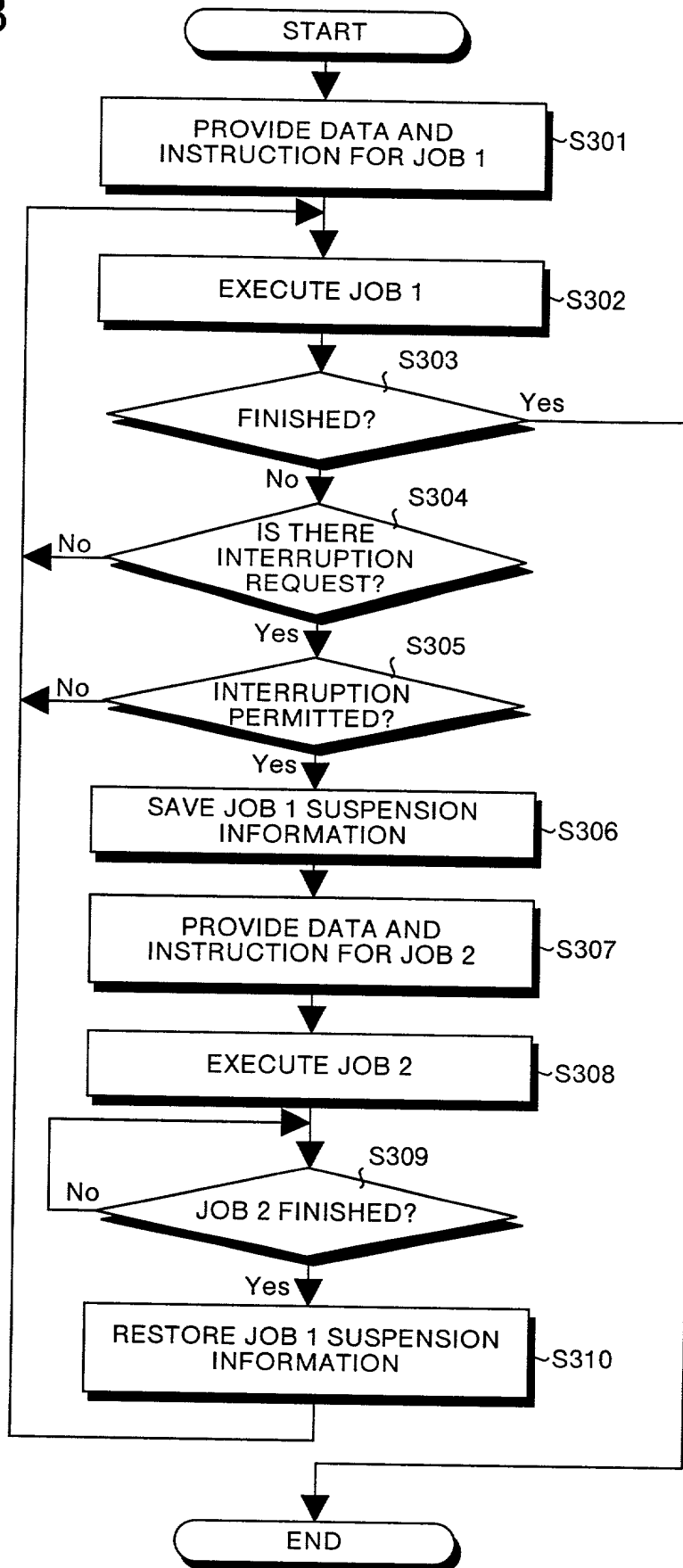


FIG.4

FROM SIMD TYPE ARITHMETIC PROCESSING SECTION 101
(CONTENTS OF ACCUMULATOR, REGISTER, DATA REGISTER)

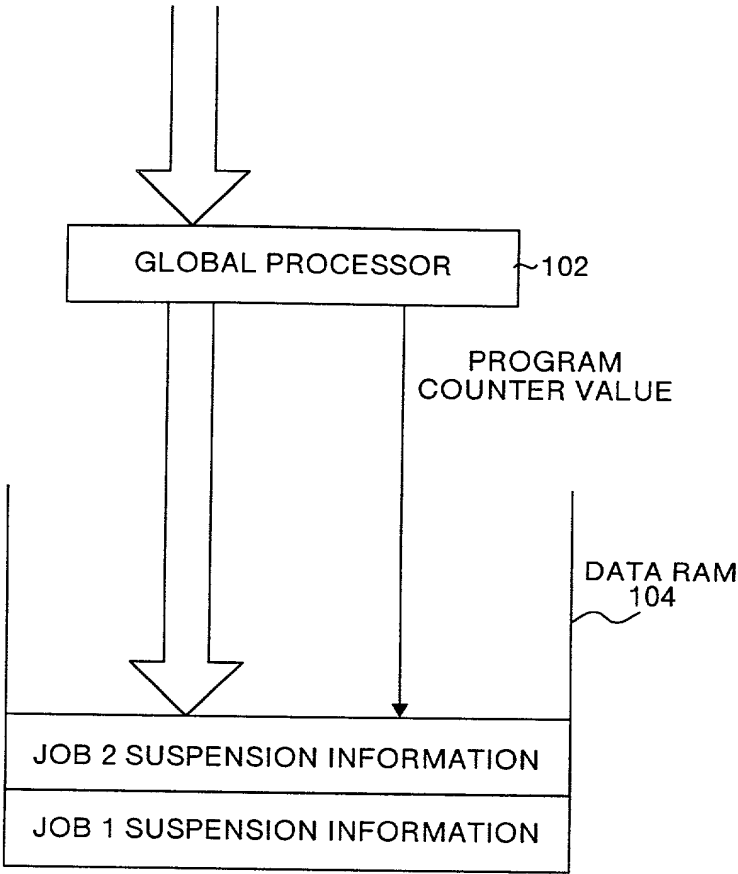


FIG.5

TO SIMD TYPE ARITHMETIC PROCESSING SECTION 101
(CONTENTS OF ACCUMULATOR, REGISTER, DATA REGISTER)

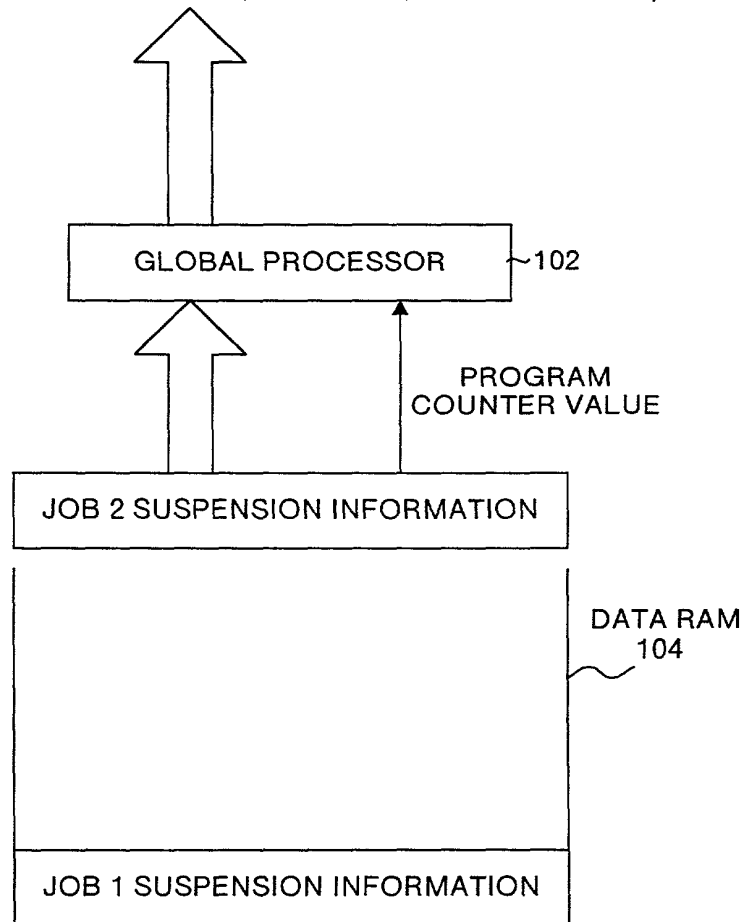


FIG.6

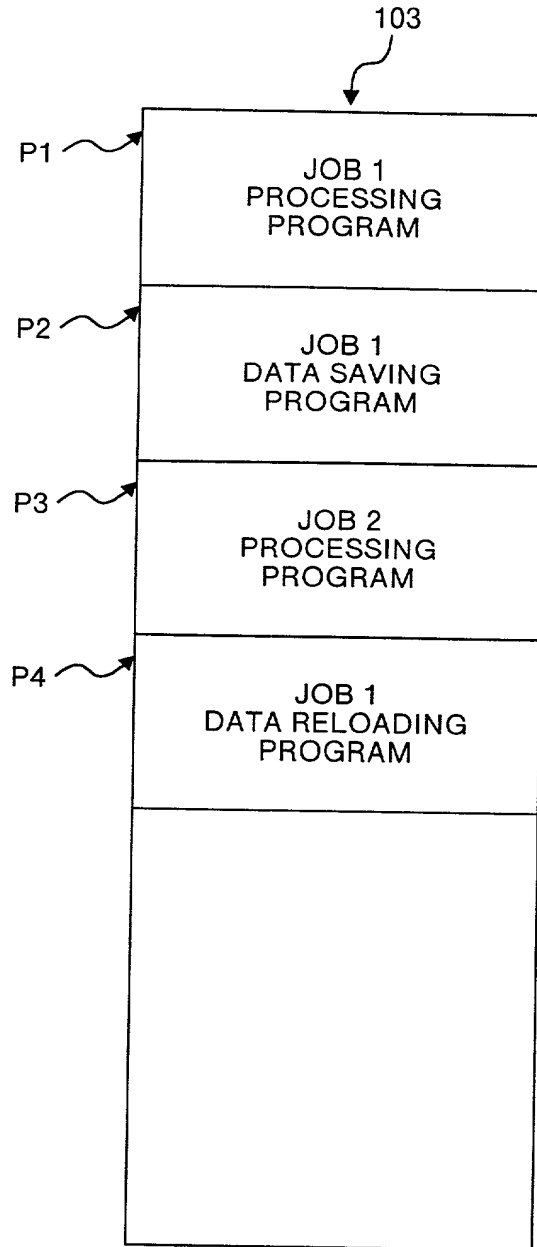


FIG.7A

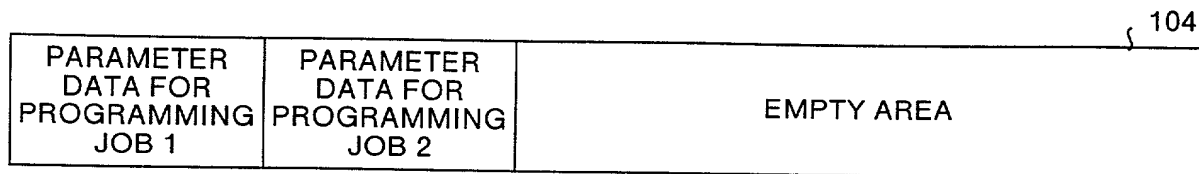


FIG.7B

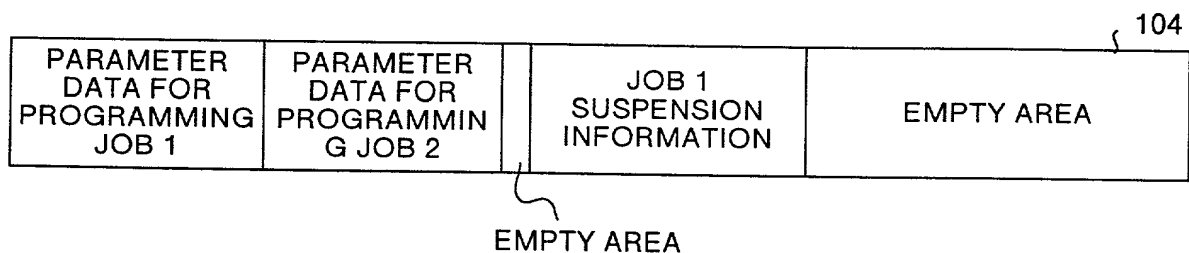


FIG.8

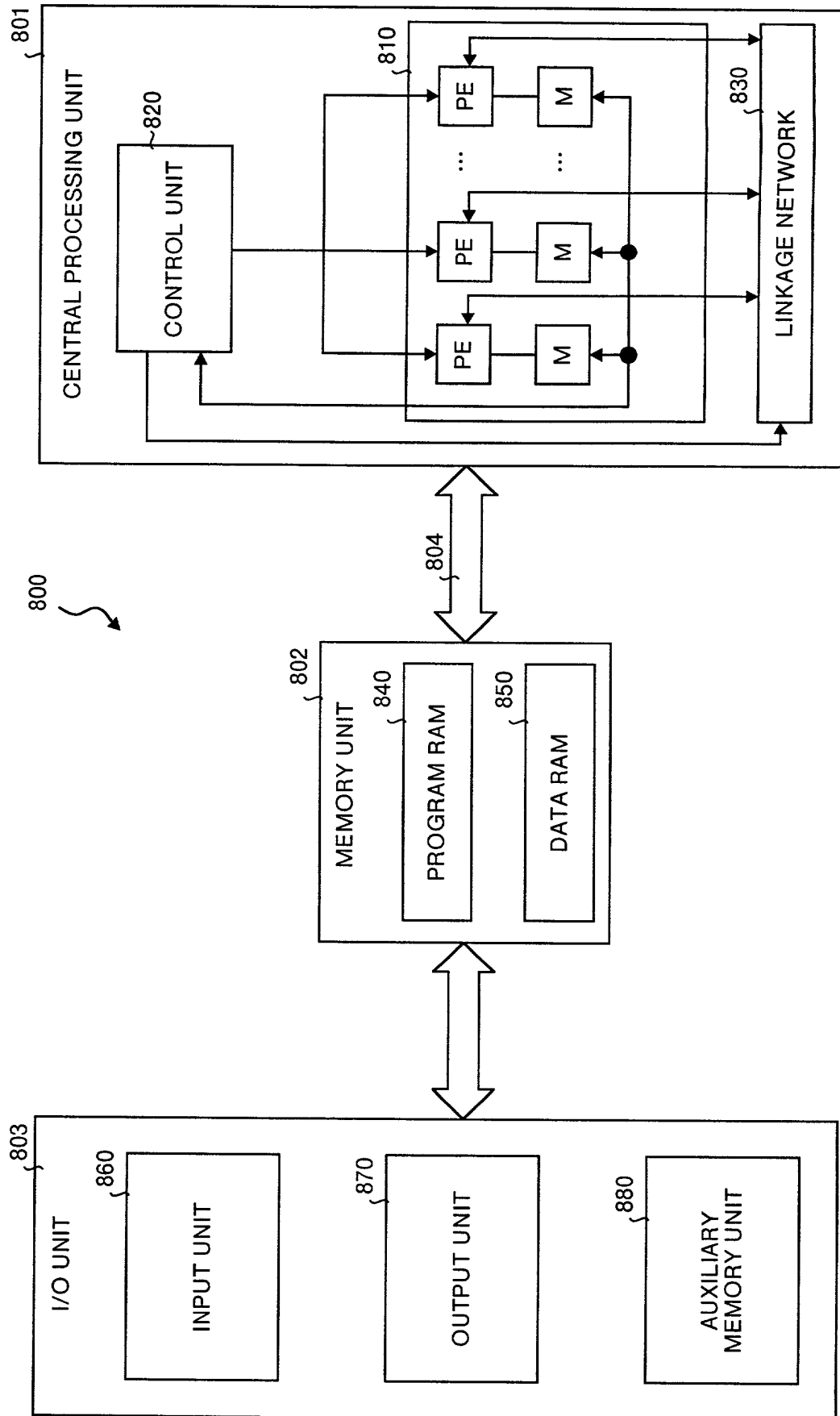
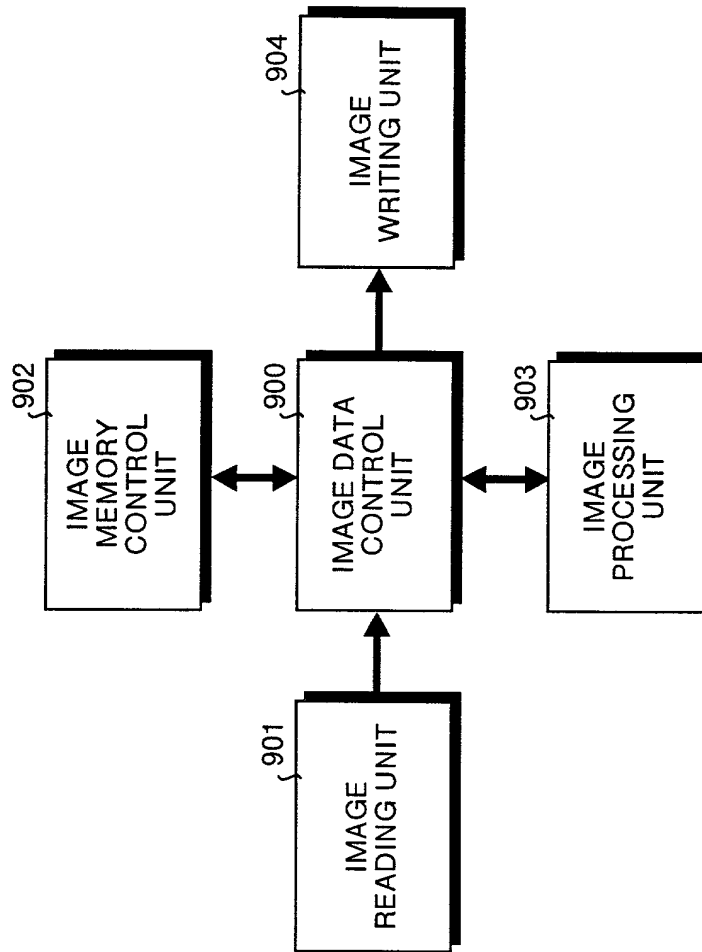
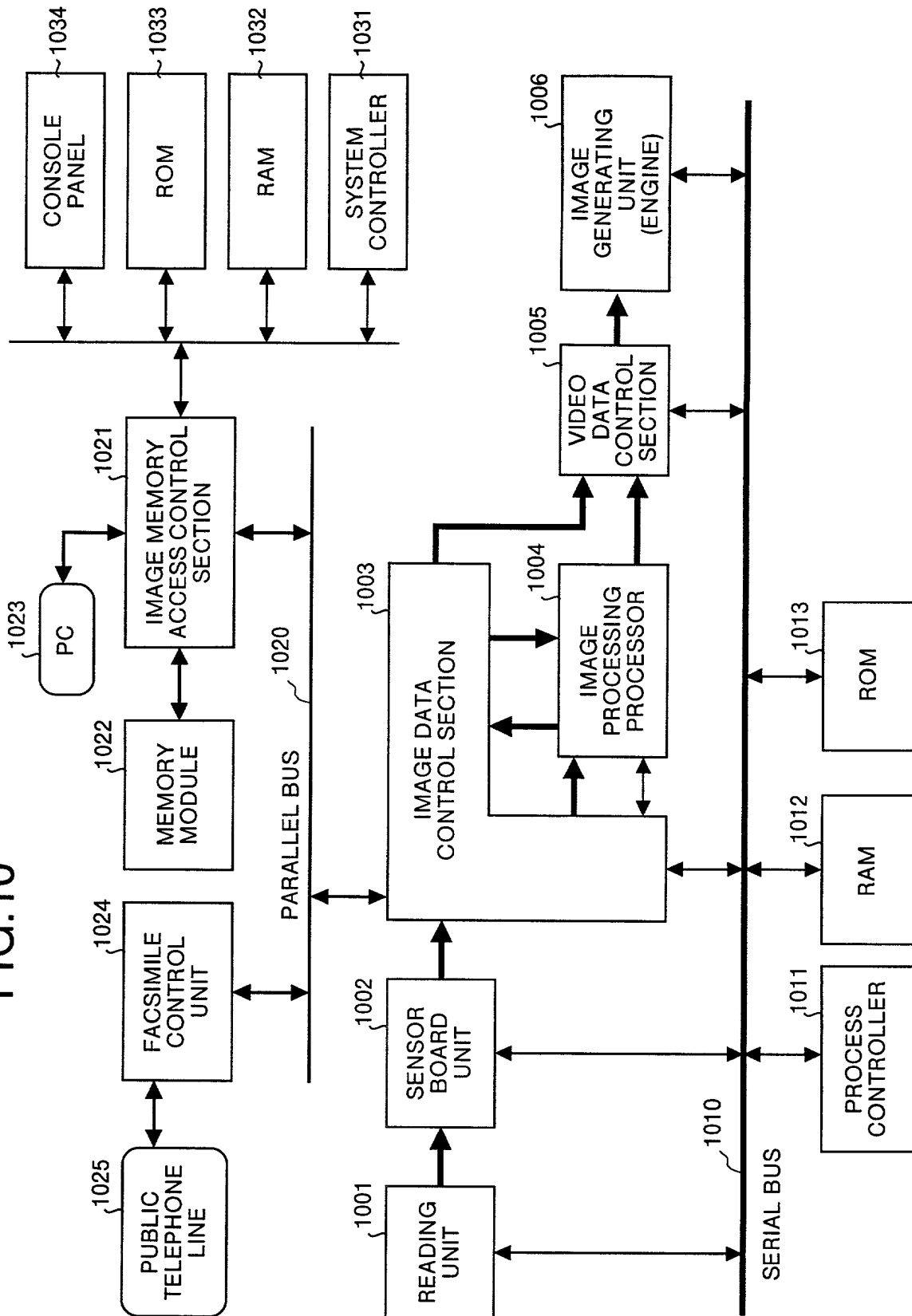


FIG.9



10/34

FIG.10



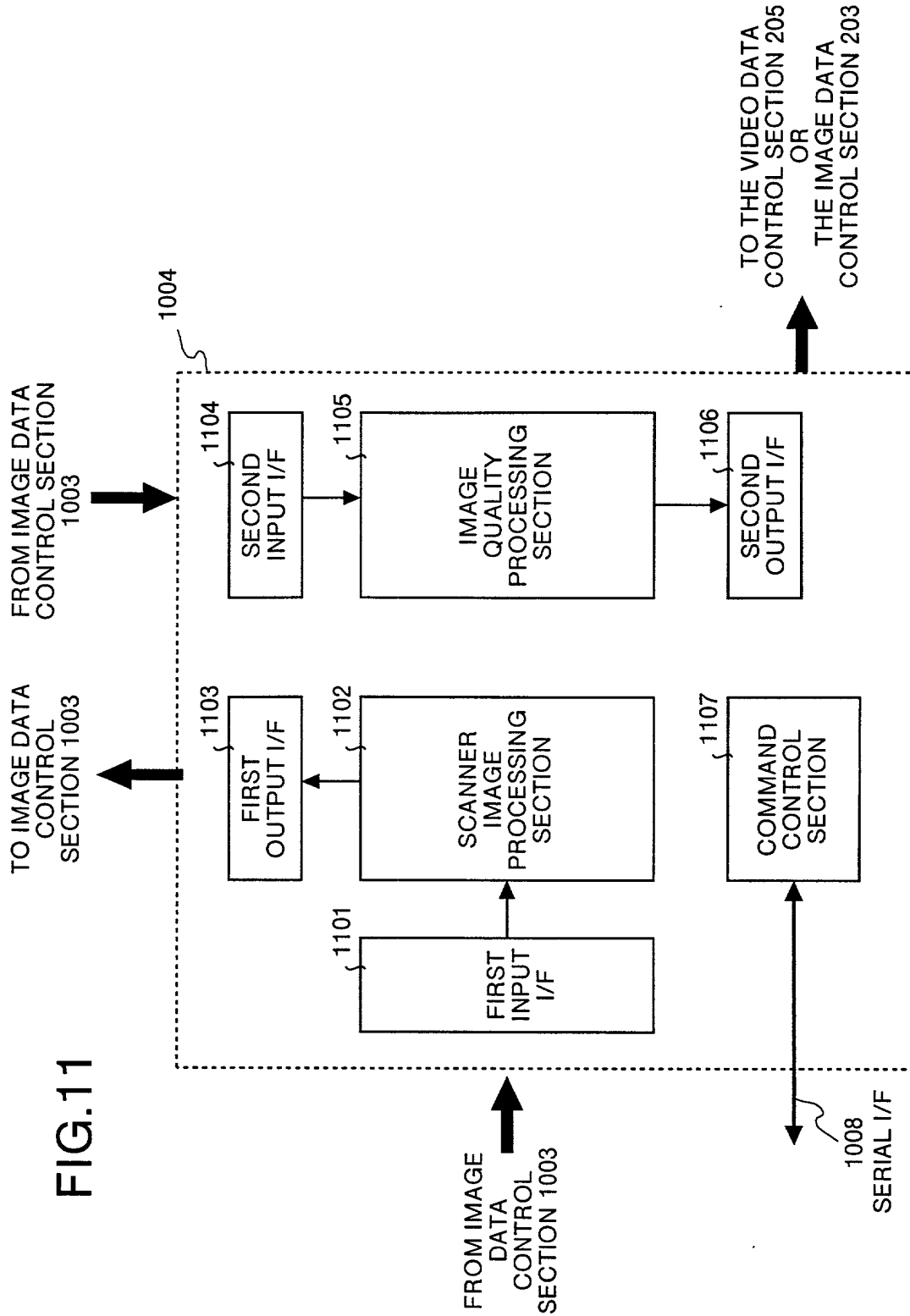


FIG.12

IMAGE DATA CONTROL SECTION 1003 OR VIDEO DATA CONTROL SECTION 1005

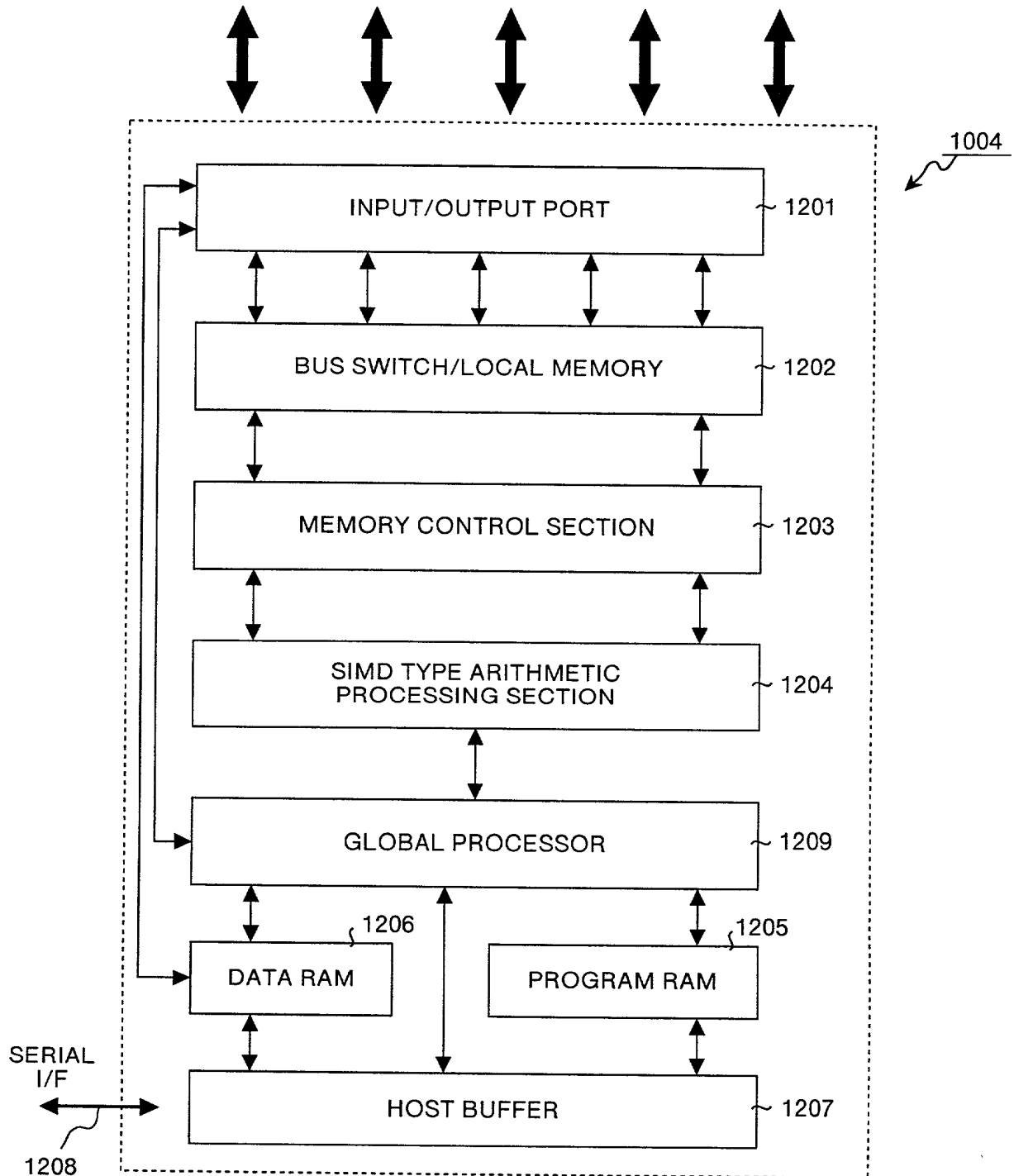


FIG.14

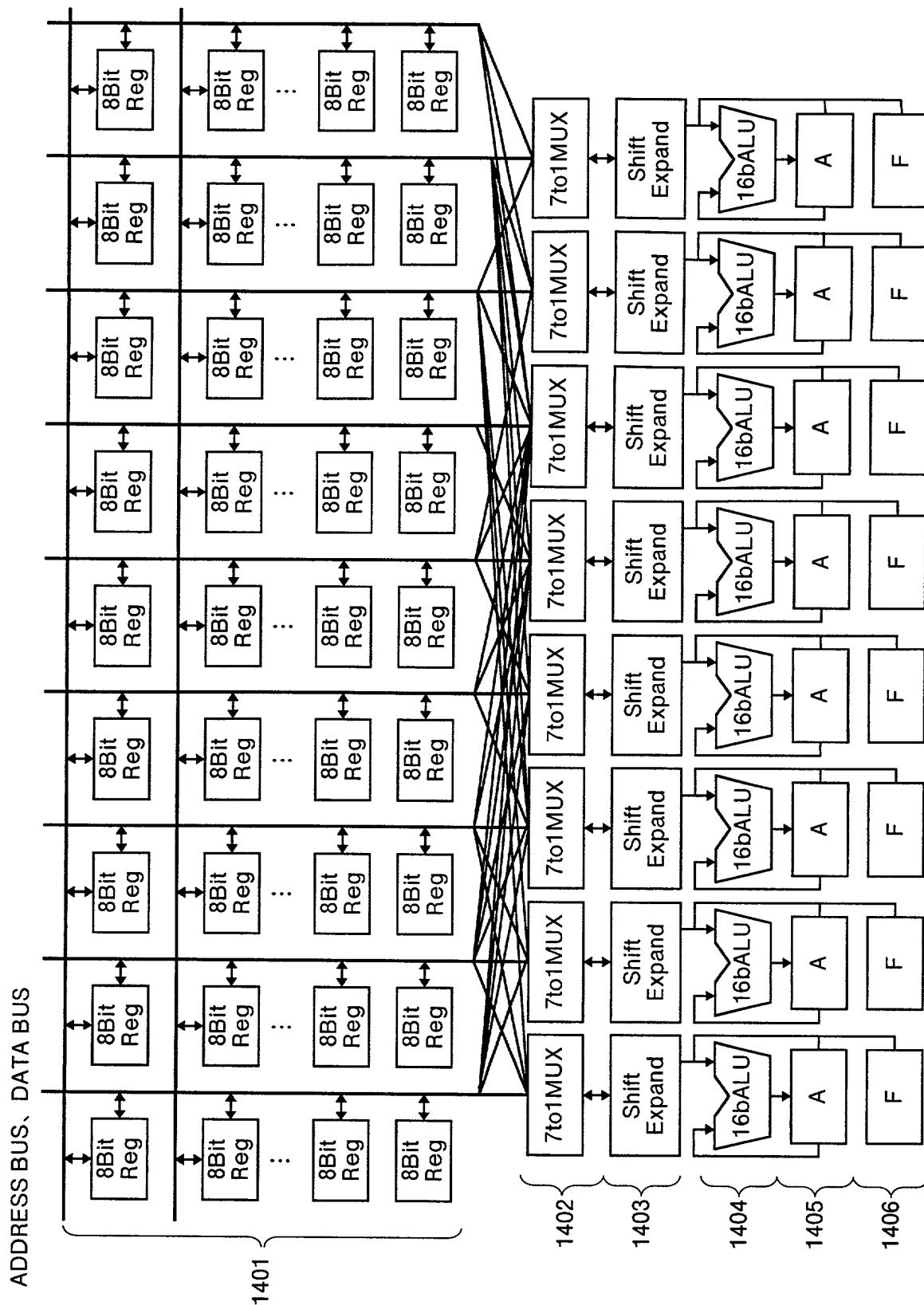


FIG.15

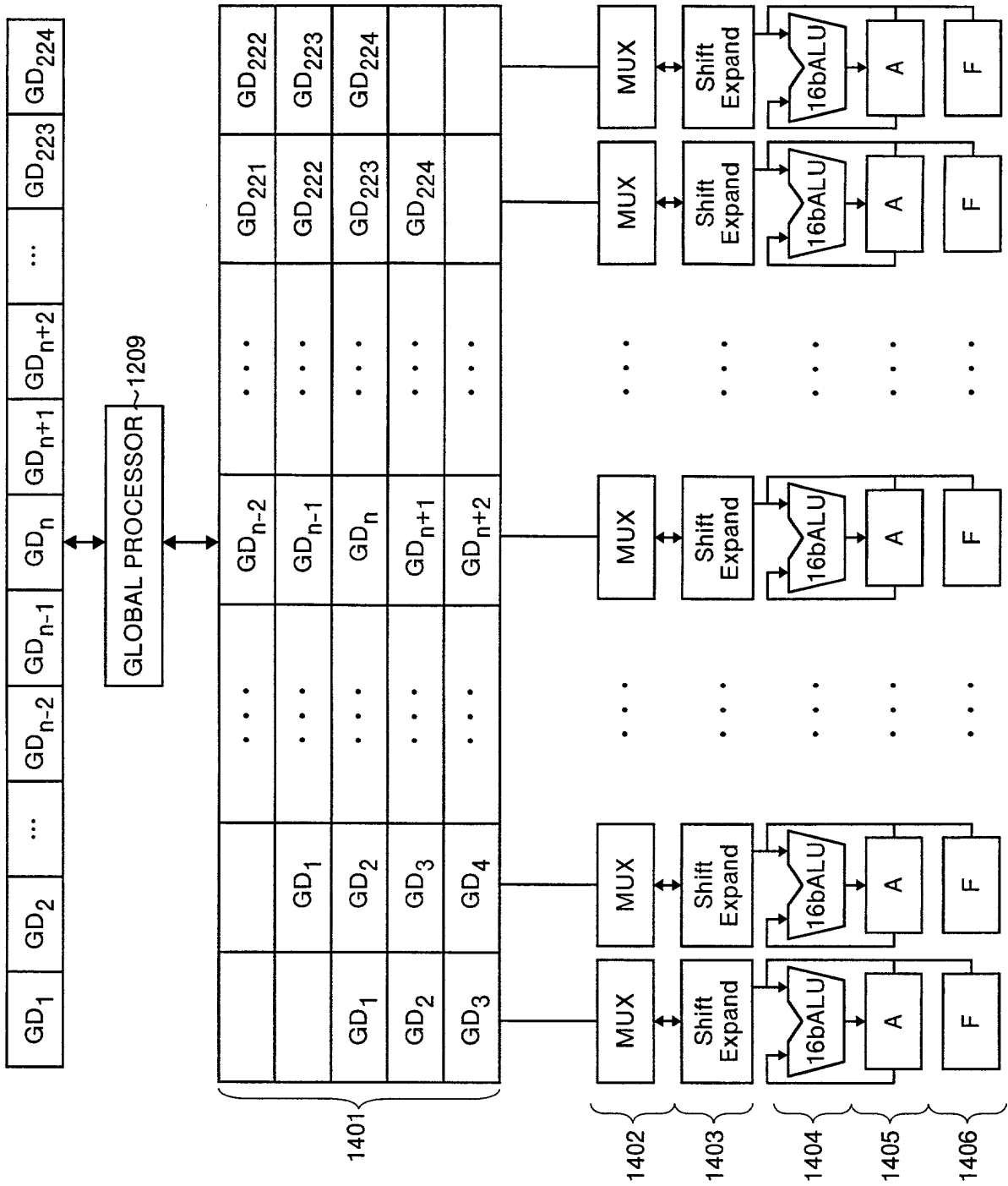


FIG.16

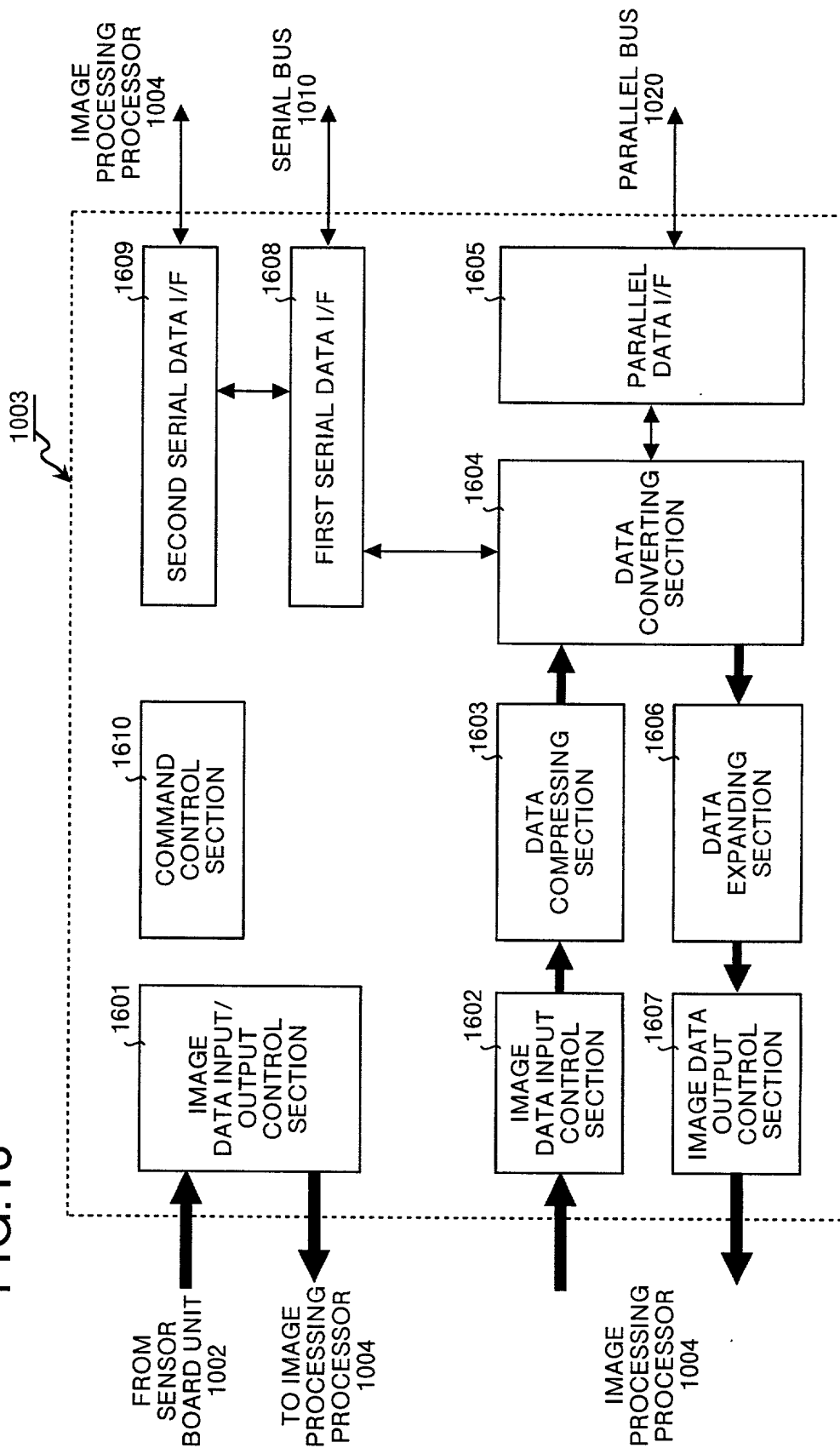


FIG.17

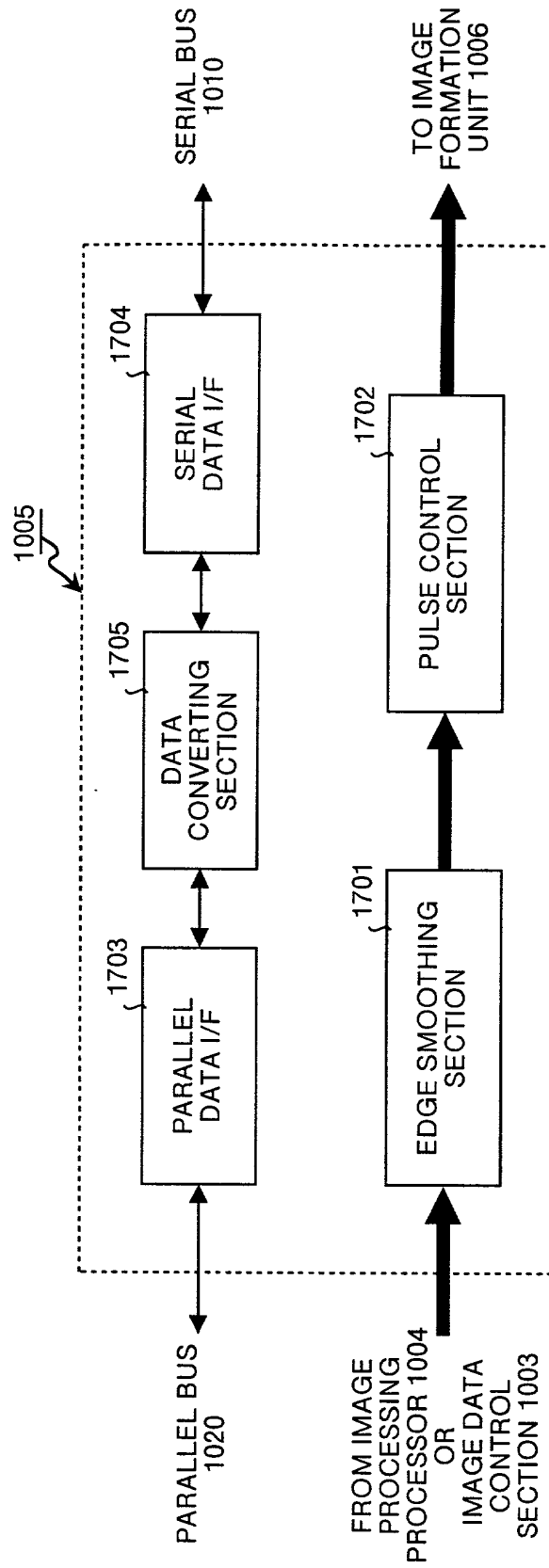


FIG.18

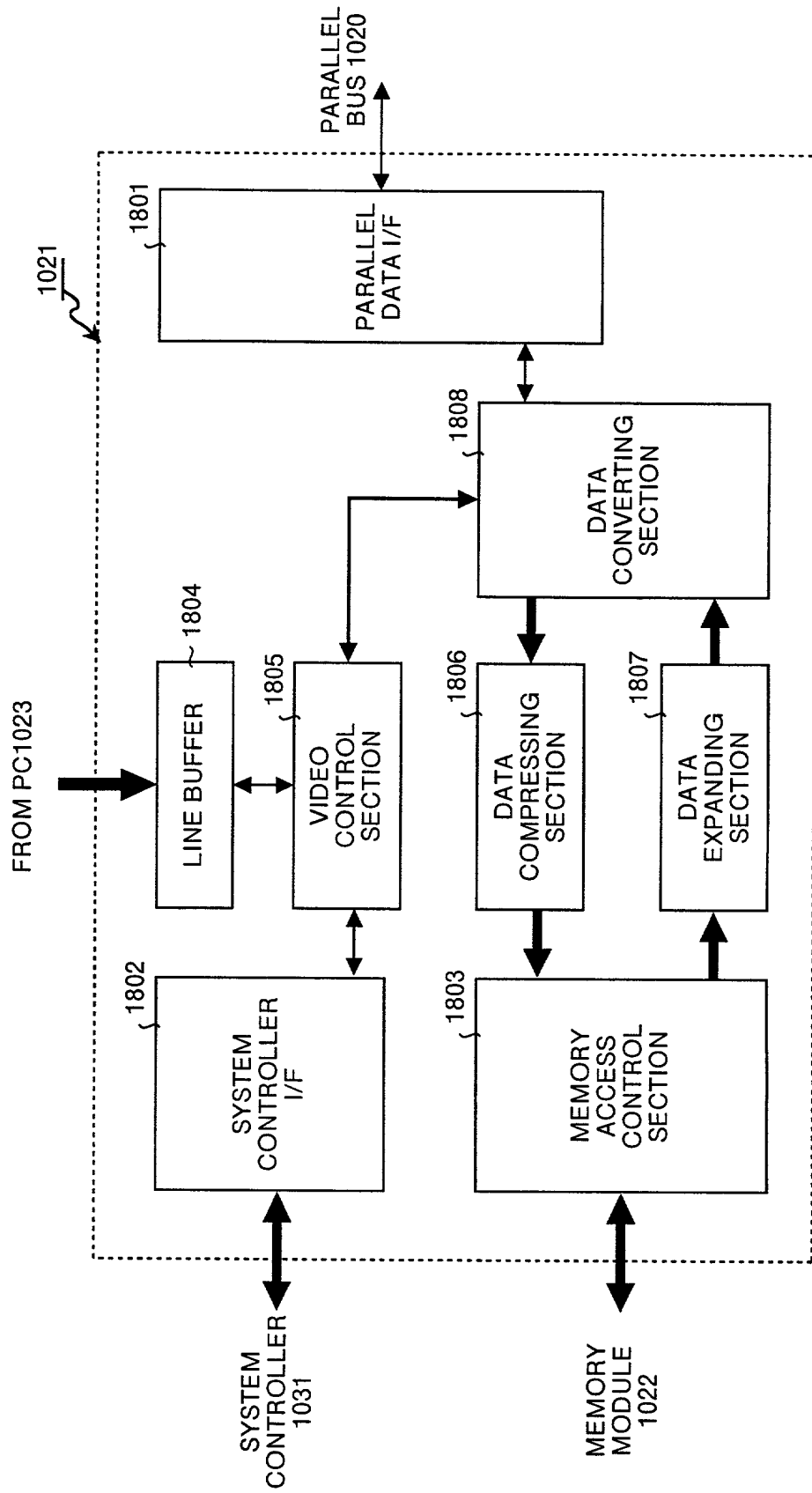


FIG.19

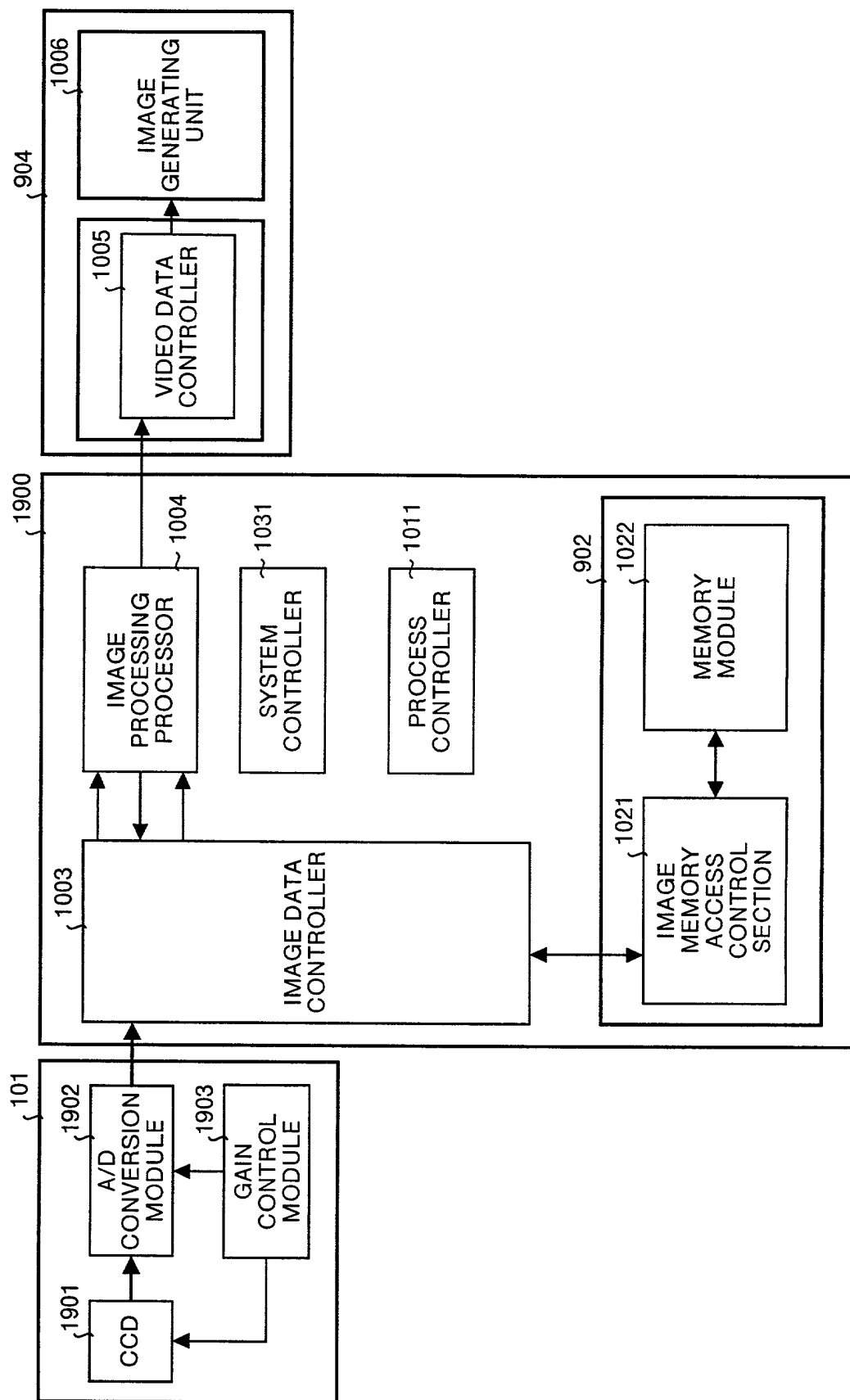


FIG.20

	j-2	j-1	COLUMN j	j+1	j+2
i-2	A	B	C	D	E
i-1	F	G	H	I	J
ROW i	K	L	M	N	O
i+1	P	Q	R	S	T
i+2	U	V	W	X	Y

FIG. 21

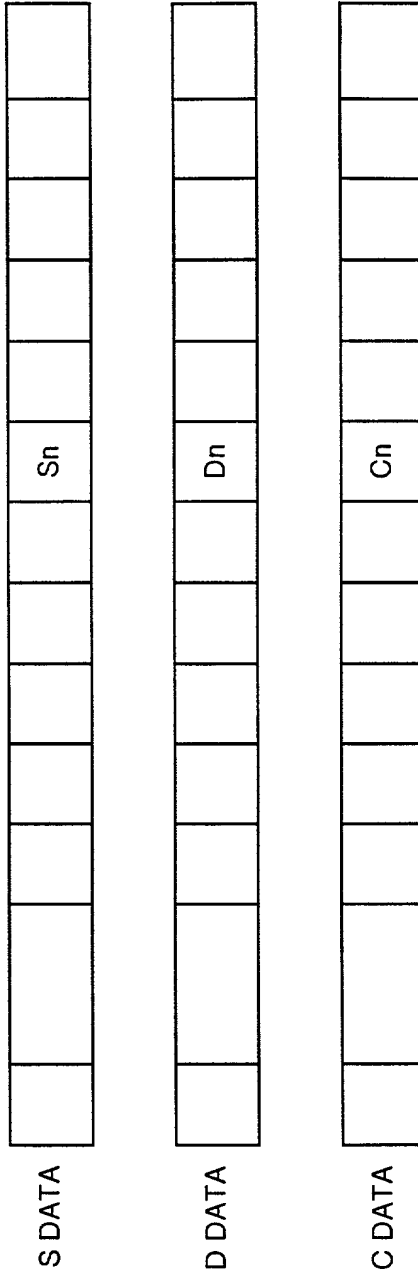
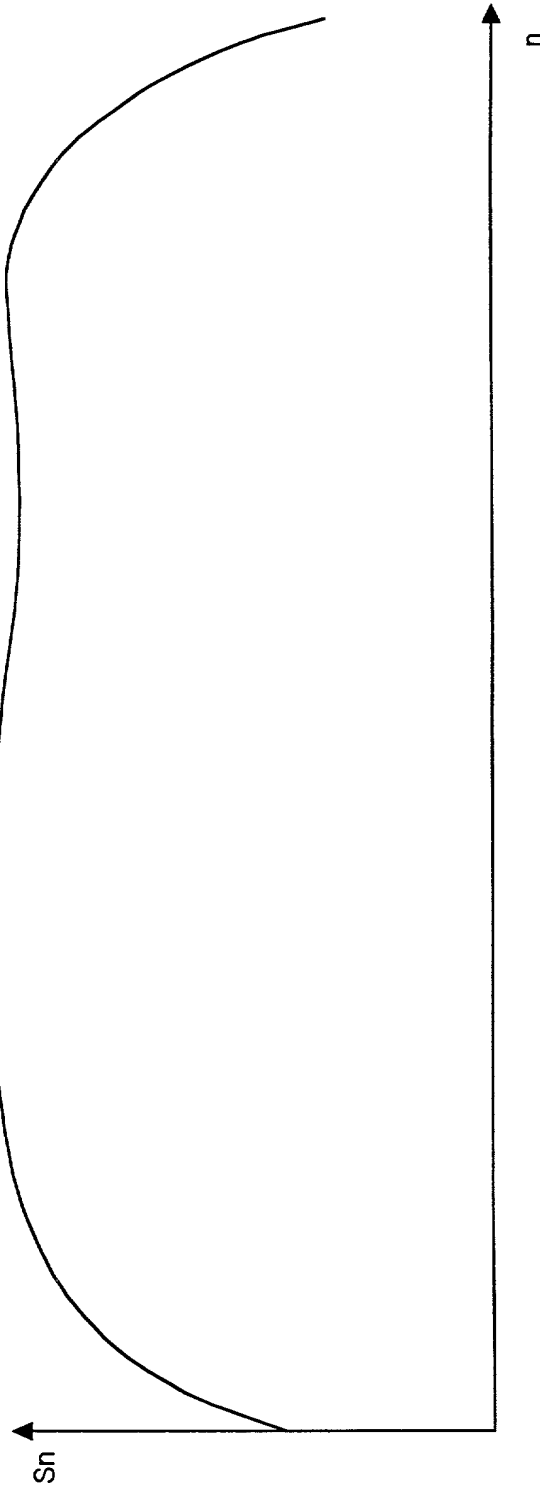
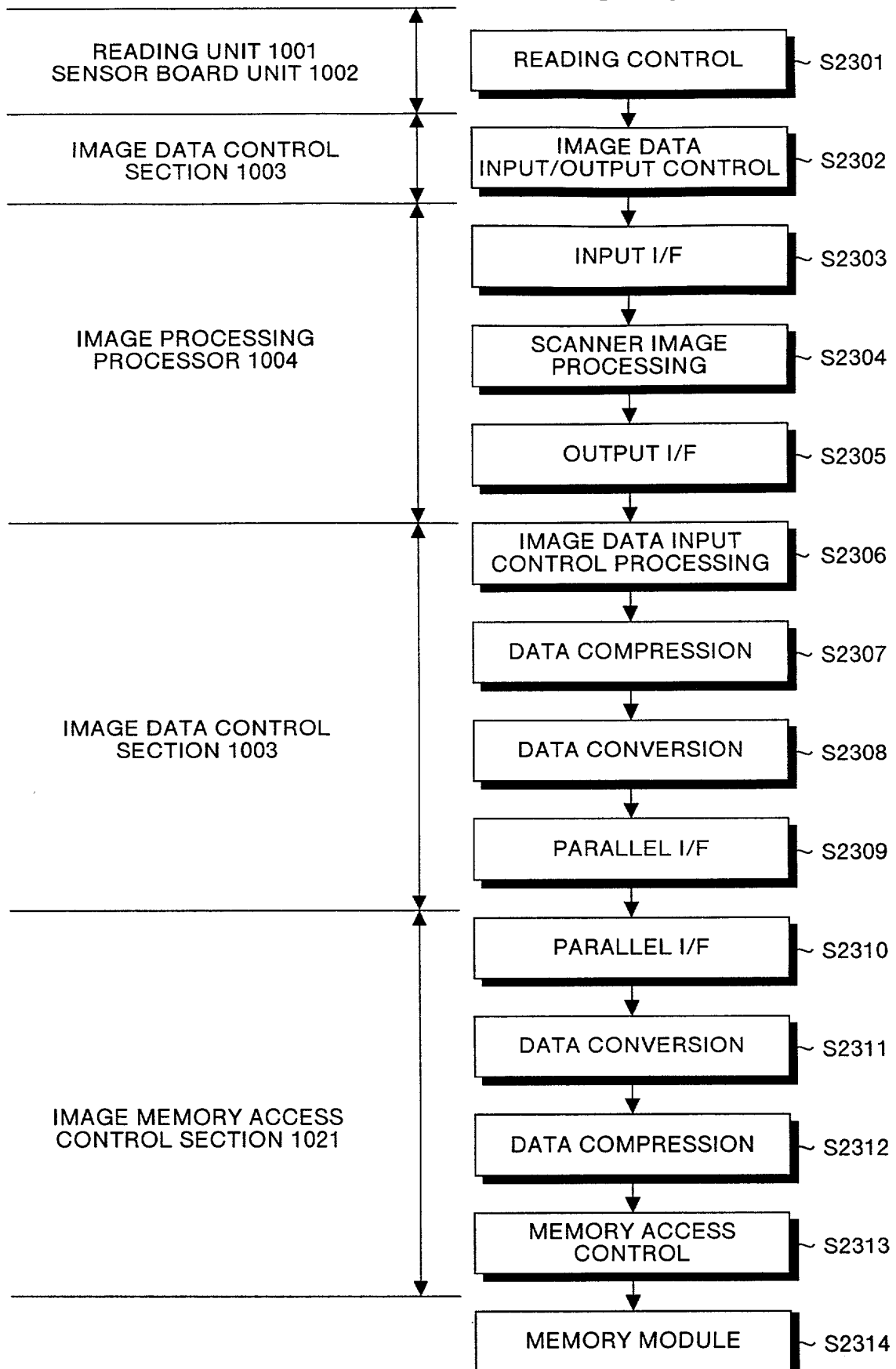


FIG.22



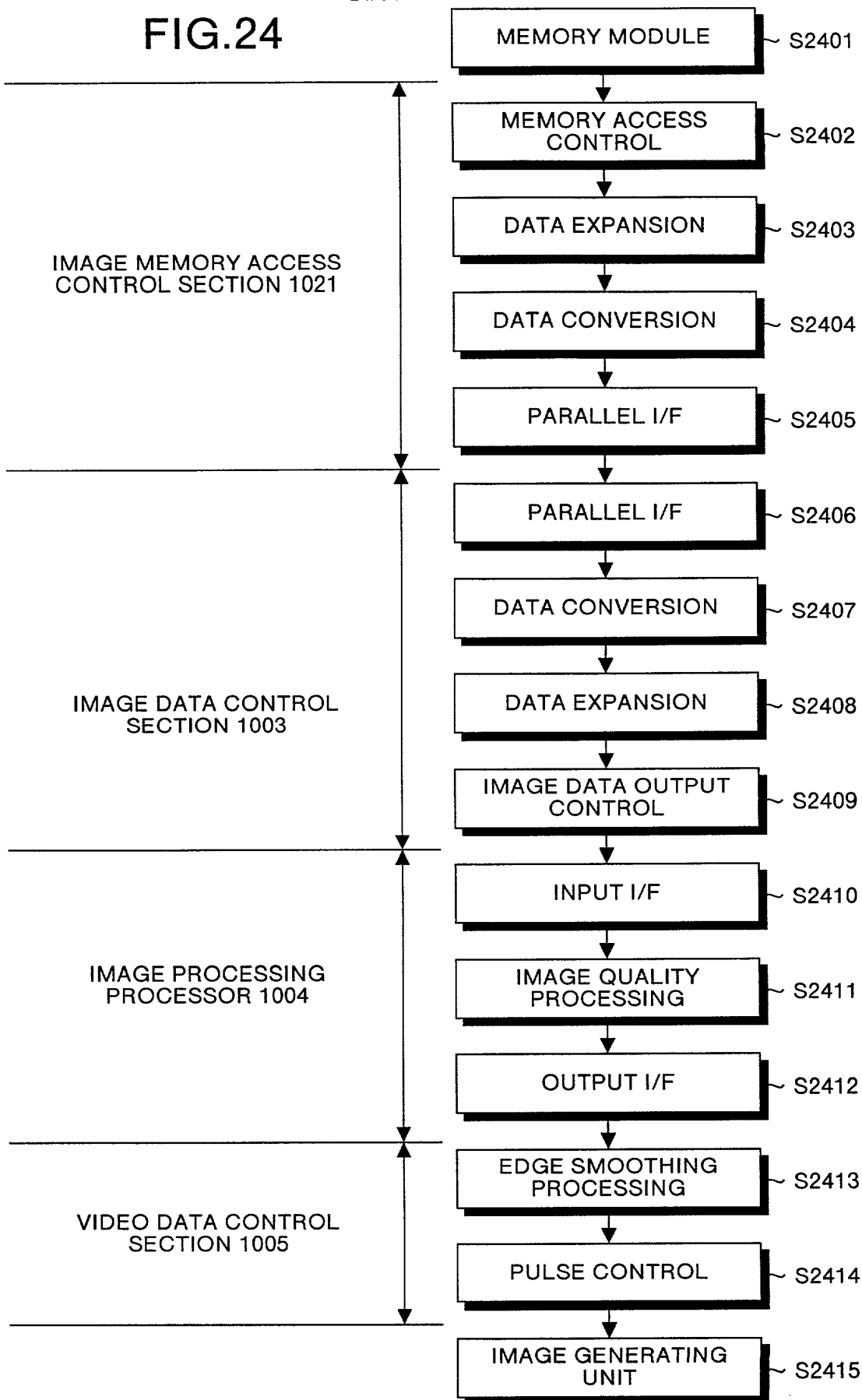
23/34

FIG.23



24/34

FIG.24



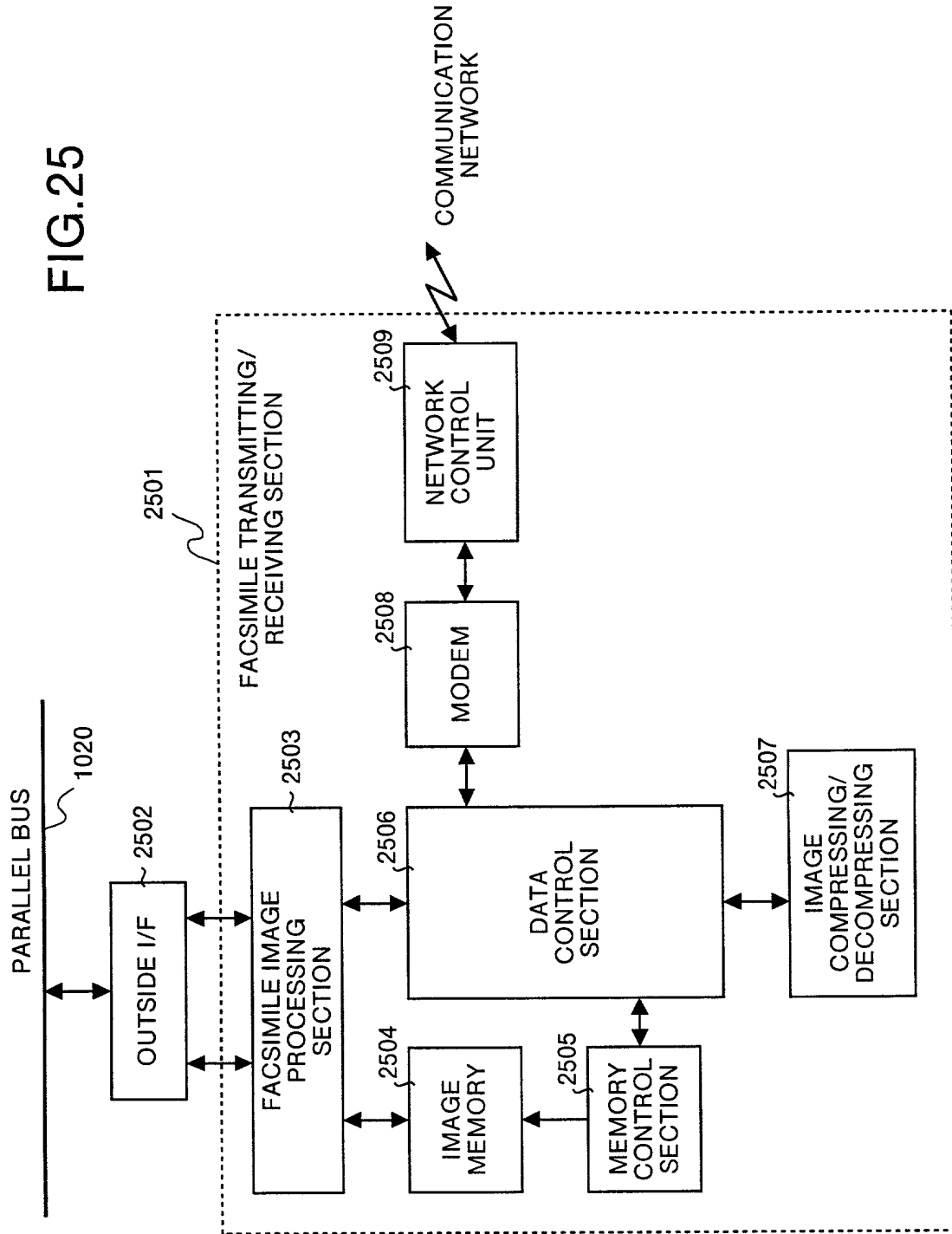
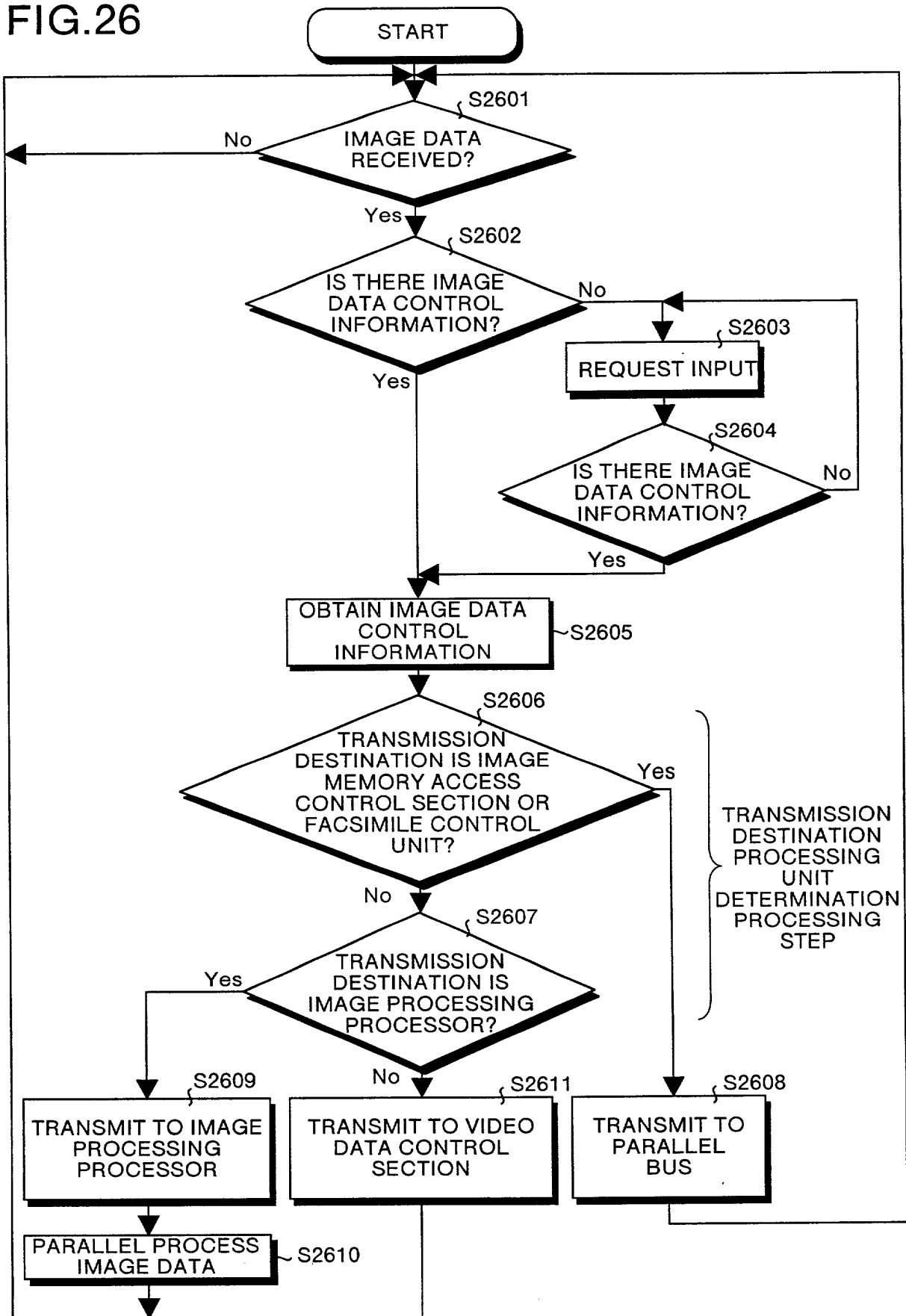


FIG.26



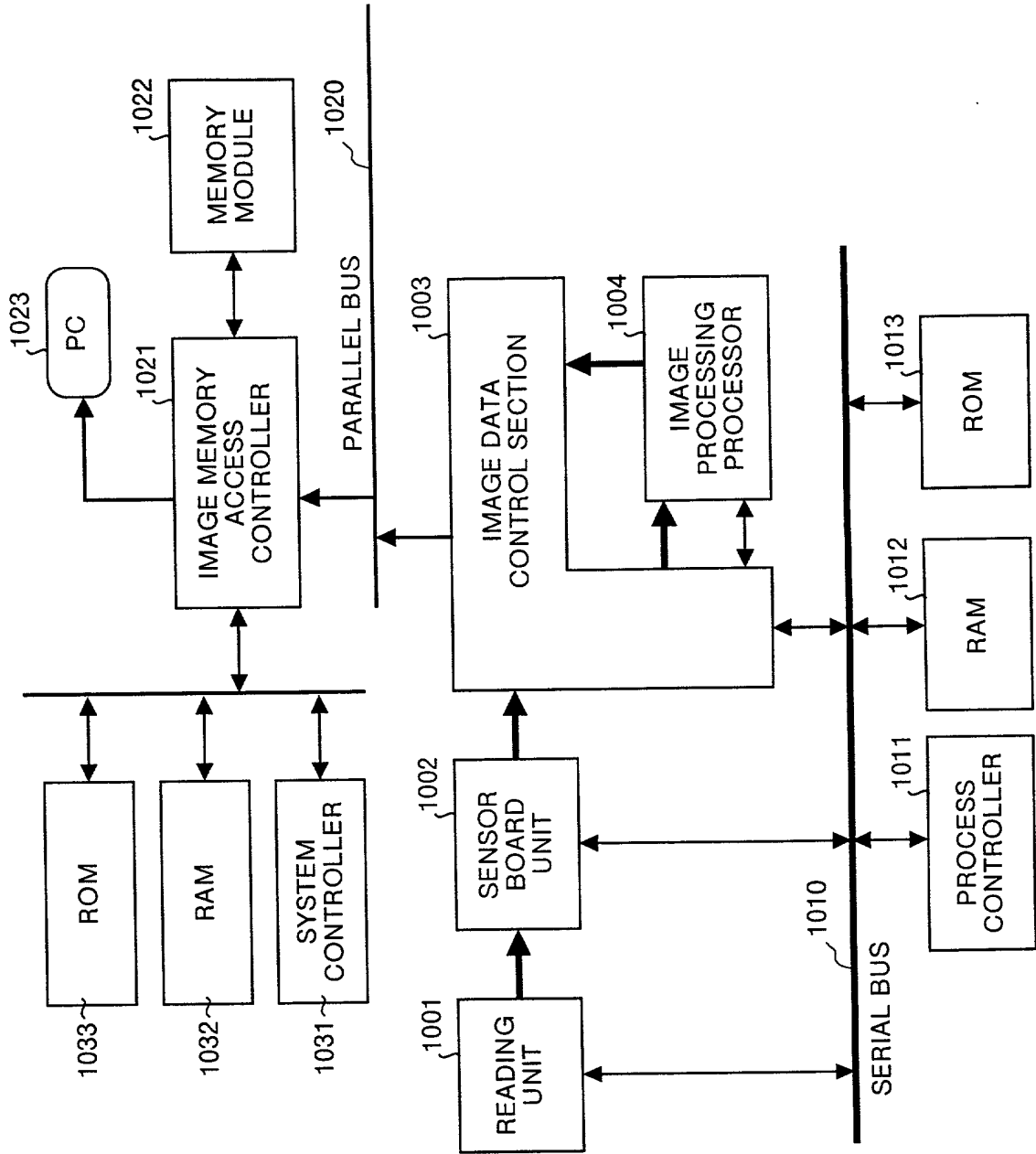


FIG.27

FIG. 27

FIG.28

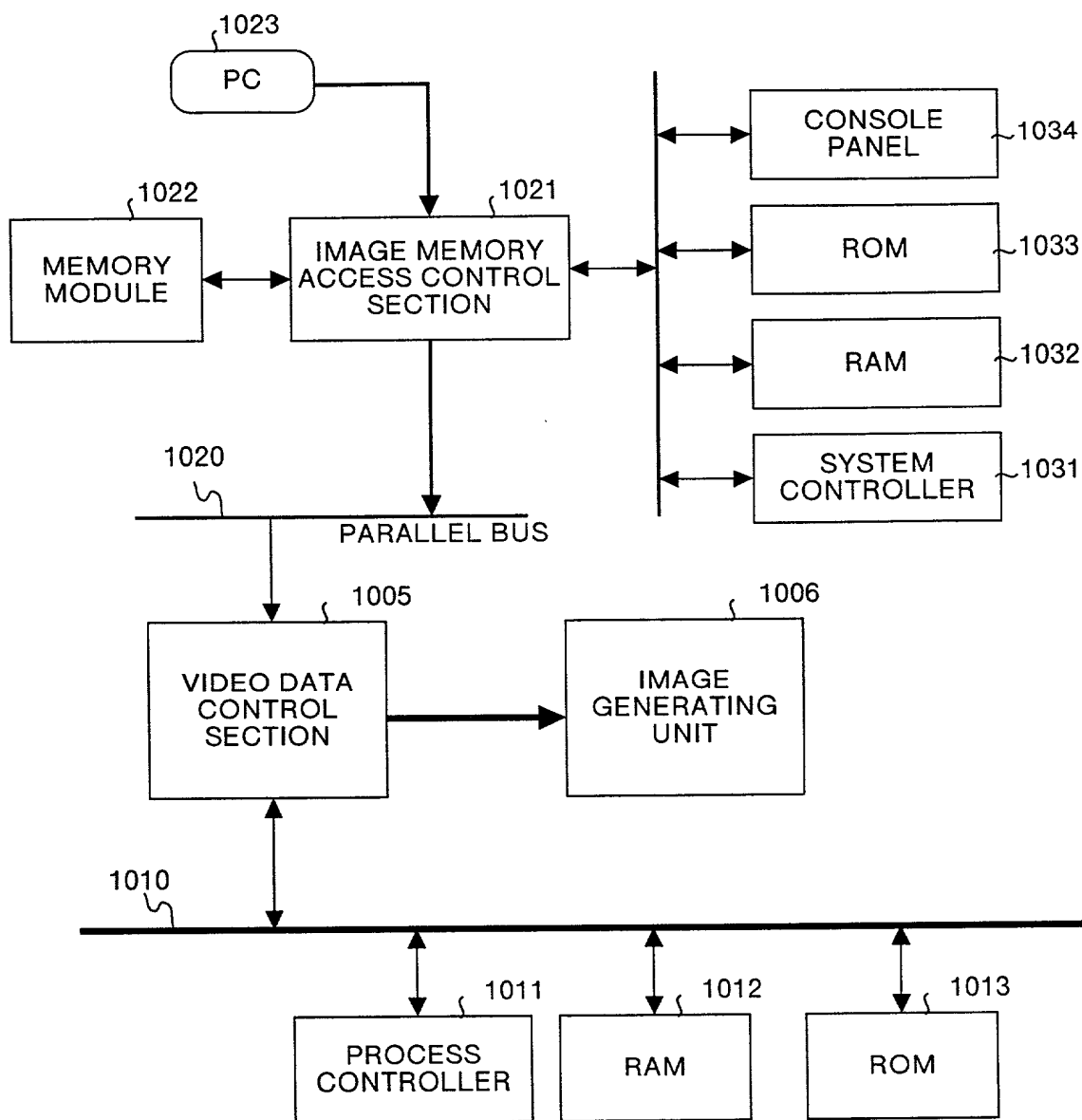


FIG.29

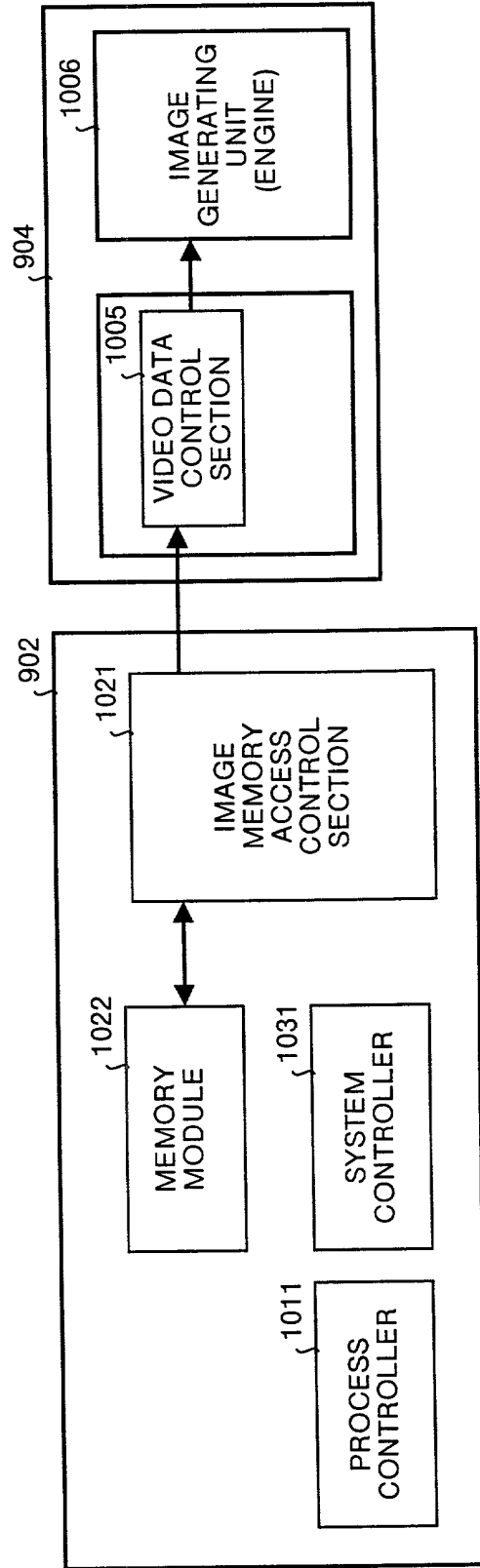


FIG.30

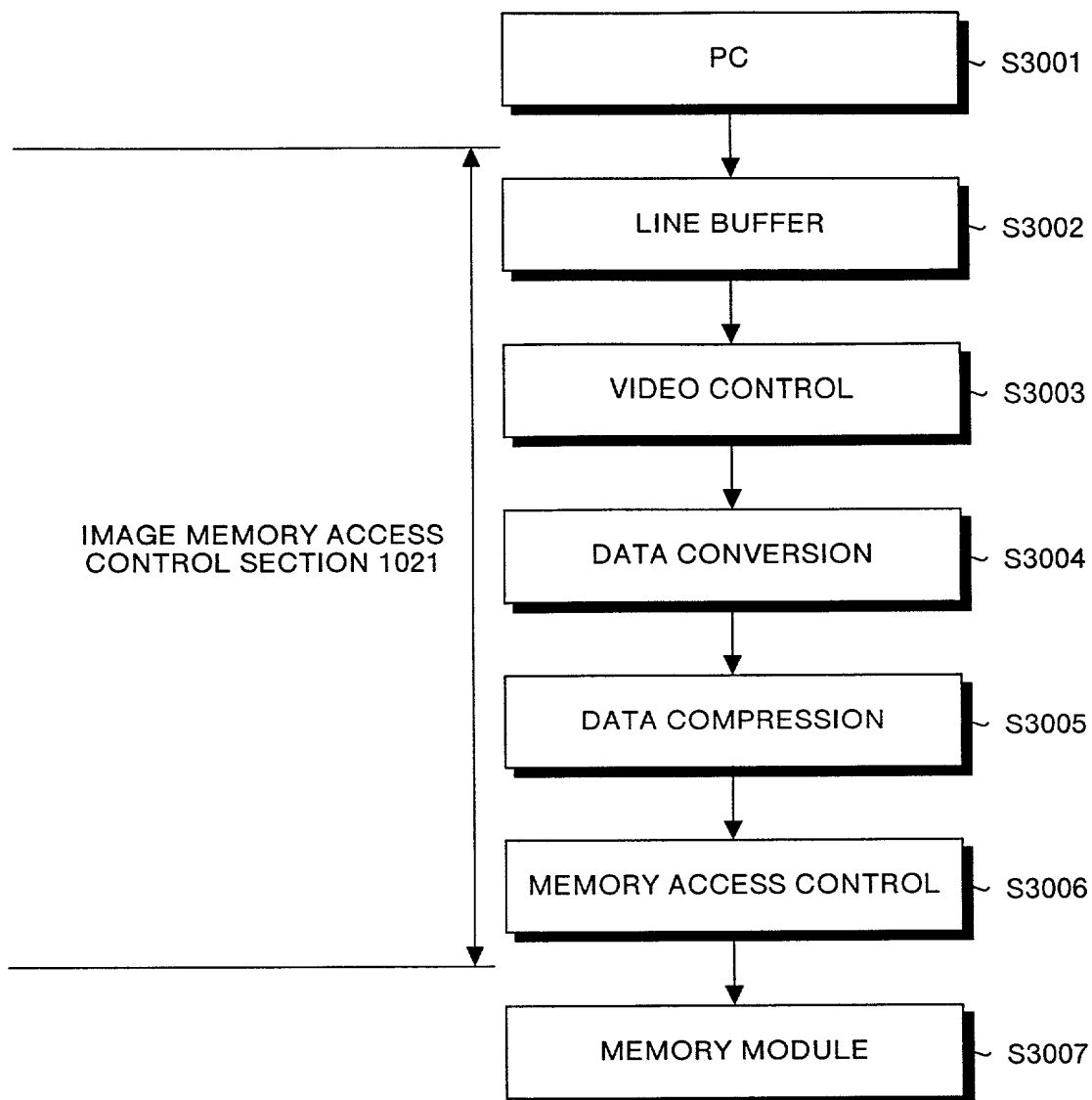


FIG.31

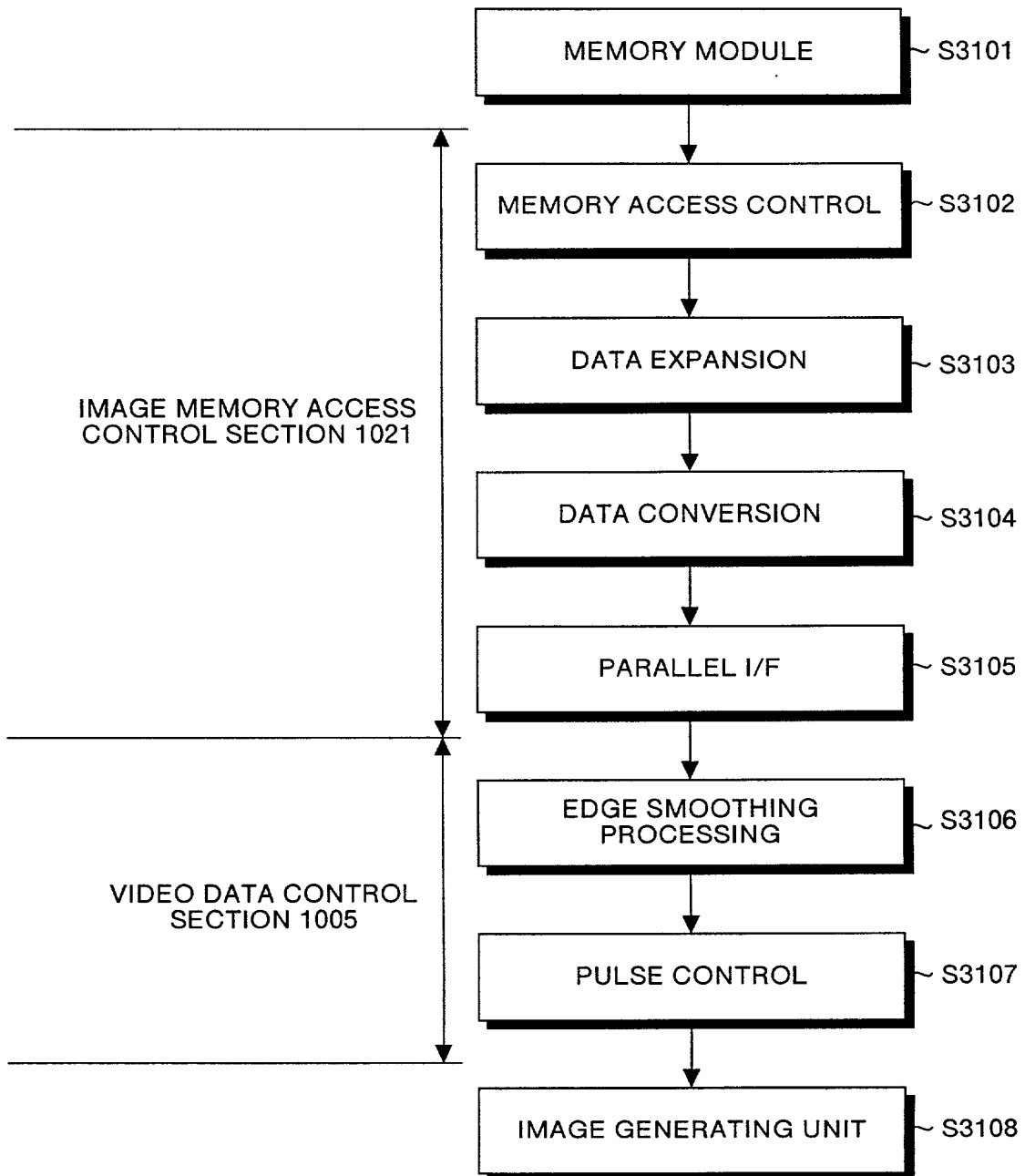


FIG.32

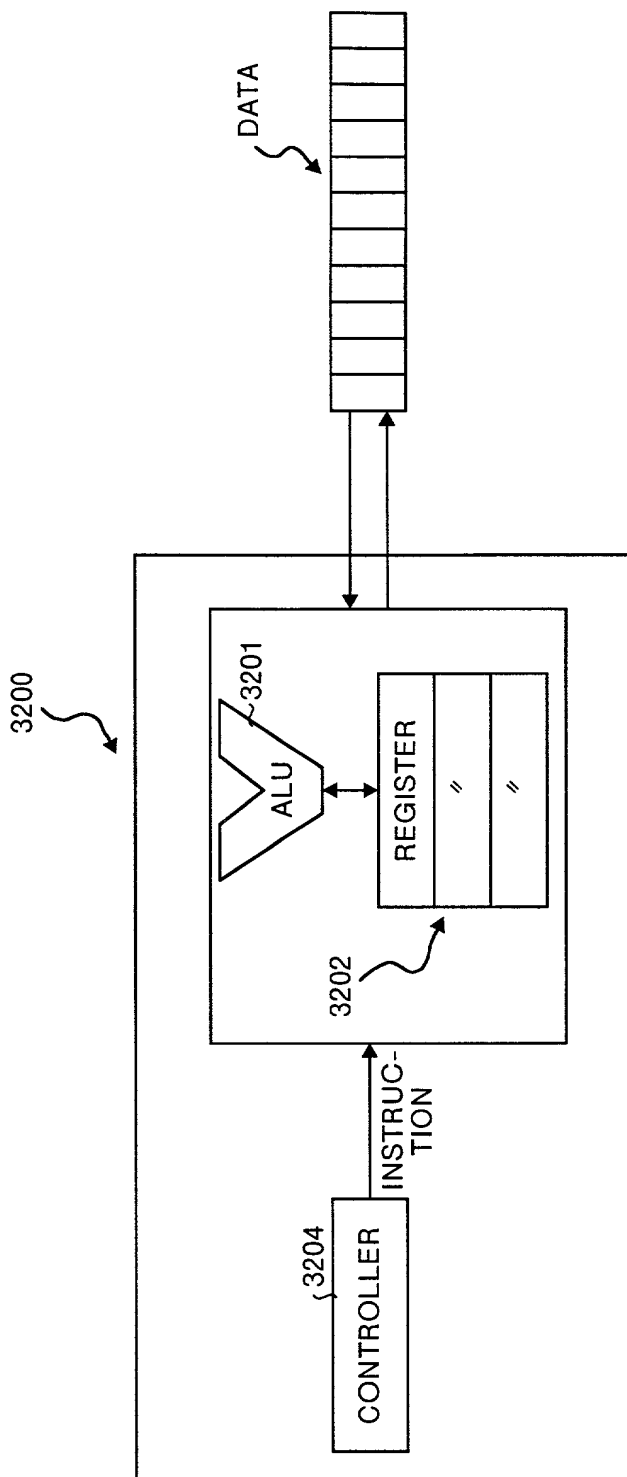


FIG.33

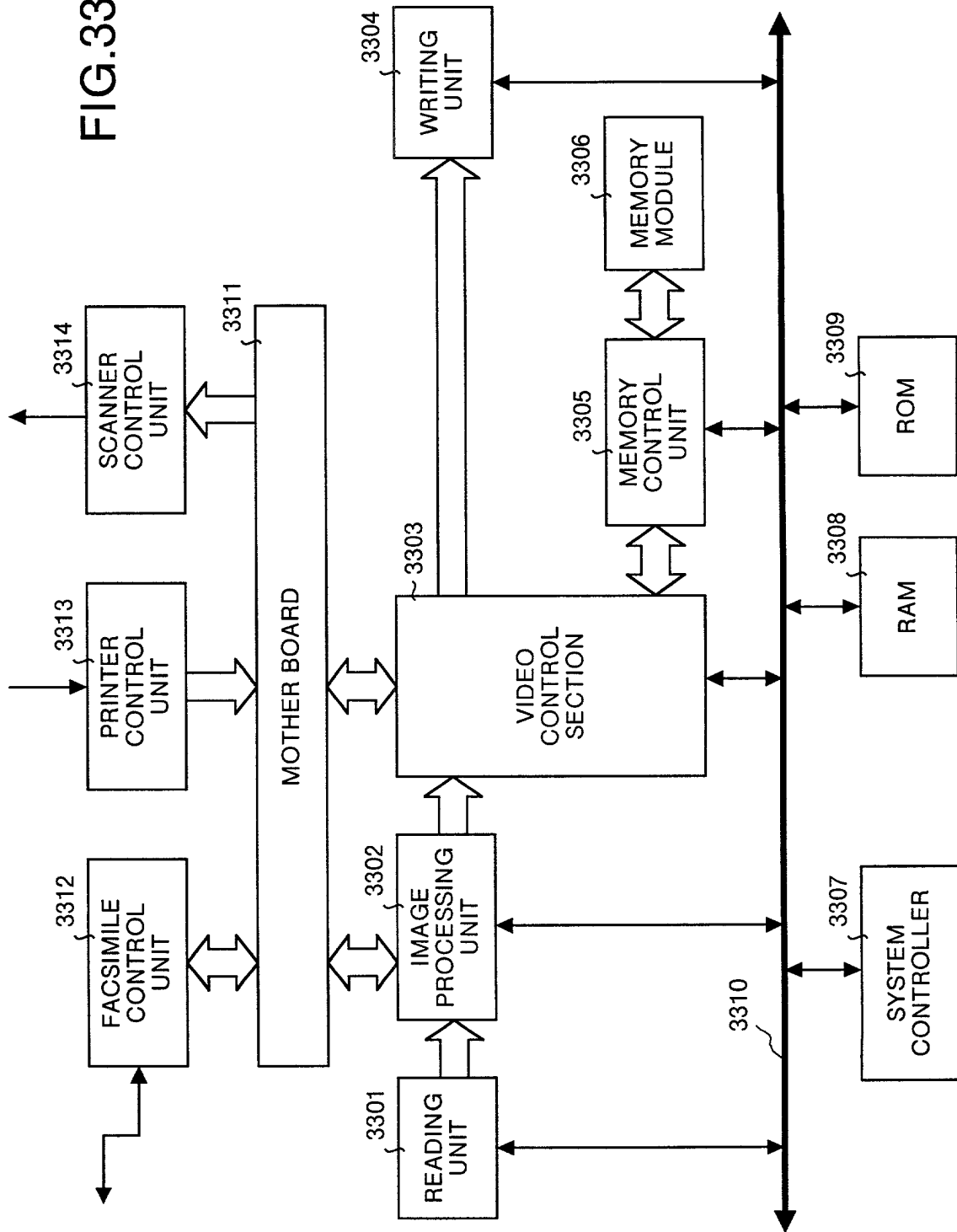


FIG.34

